VIP CLUB

Year	Computer	CPU Technology	Memory	Configuration	Op Codes	Performance	Notes
1950	ERA 1101	Vacuum Tube	Drum	24 bits x 16 kw	43	0.0002	
1951	UNIVAC I	Vacuum Tube	Mercury Line				
1953	UNIVAC 1102	Vacuum Tube	Drum	24 bits x 16 kw	43	0.0002	
1953	UNIVAC 1103	Vacuum Tube	Electrostatic/Drum	36 bits x 16 kw	41	0.014	
1953	Burroughs UDEC	Vacuum Tube	Drum				
1954	UNIVAC 1103A	Vacuum Tube	Core/Drum	36 bits x 16 kw	41	0.014	and 12 kw core
1954	Burroughs E-101	Vacuum Tube	Drum				
1954	Datatron	Vacuum Tube	Drum				
1955	Burroughs UDEC	Vacuum Tube	Core/Drum				
1956	UNIVAC 1104	Vacuum Tube	Drum	30 bits x 16 kw	41	0.0002	
1956	UNIVAC File 0	Vacuum Tube	Drum				
1957	UNIVAC File 1	Vacuum Tube	Core				
1957	UNIVAC II	Vacuum Tube	Core				
1957	UNIVAC Athena	Transistor	Core/Drum				
1957	UNIVAC Bogart	Switched Cores	Core				
1957	Burroughs AN/GSQ-33	Transistor	Drum				
1958	UNIVAC Solid State	Amplifiers	Drum				
1958	Burroughs 220	Vacuum Tube	Core				
1958	UNIVAC 1105	Vacuum Tube	Core/Drum	36 bits x 16 kw	50	0.025	and 12 kw Core
1960	UNIVAC LARC	Transistor	Core				
1961	UNIVAC 490	Transistor	Core				
1962	UNIVAC 1107	Transistor	Core	36 bits x 64kw	117	0.2	
1962	UNIVAC III	Transistor	Core				
1962	Burroughs D825	Transistor	Core				
1962	Burroughs 270	Transistor	Core				
1963	Burroughs 5000	Transistor	Core				
1963	UNIVAC 418	Transistor	Core				
1963	UNIVAC 1004	Transistor	Core/plug board				
1963	UNIVAC 1050	Transistor	Core				
1965	**UNIVAC 1108**	**Transistor/IC**	**Core**	**36 bits x 64kw**	**149**	**1**	**Double Precision FP**
1966	UNIVAC 1005	Transistor	Core				
1968	UNIVAC 1108	**Transistor/IC**	Core	36 bits x 256kw	151	1 to 2.4	3 CPU, 2 IOC w/76 ch
1969	UNIVAC 1106	**Transistor/IC**	Core	36 bits x 256kw	151	0.8 to 1.4	2 CPU, 1 IOC w/32 ch
1970	UNIVAC 1106	**Transistor/IC**	Core	36 bits x 512kw	151	0.6 to 1.0	2 CPU, 1 IOC w/32 ch
1972	Sperry 1110		Core/Plated Wire	36 bits x 1 MW	206	1.4 - 6.4	6 CAU, 4 IOAU w/96 ch

Year	Computer	CPU Technology	Memory	Configuration	Op Codes	Performance	Notes
1975	Sperry 1100/10		DRAM	36 bits x 512kw	151	0.8 to 1.4	2 CPU, 1 IOC w/32 ch
1975	Sperry 1100/20		DRAM	36 bits x 512kw	151	1 to 1.6	2 CPU, 1 IOC w/32 ch
1975	Sperry 1100/40		DRAM/Plated Wire	36 bits x 1 MW	206	1.4 - 6.4	6 CAU, 4 IOAU w/96 ch
1976	Sperry 1100/80		DRAM	36 bits x 4 MW	201	2.6 - 8	4 IP, 4 IOU w/128 ch
1979	Sperry 1100/60		DRAM	36 bits x 8 MW	202	0.6 - 5	4 IP, 4 IOU w/48 ch
1981	Sperry 1100/70		DRAM	36 bits x 8 MW	202	0.6 - 5	4 IP, 4 IOU w/48 ch
1983	Sperry 1100/90		DRAM	36 bits x 16 MW	266	8.6 - 26	4 IP, 4 IOU w/96 ch
1984	Sperry System-11			36 bits x 4 MW	268	0.6 - 0.8	2 IP, 1 IOU w/8 ch
1987	Sperry 2200/200			36 bits x 12 MW	281	1.6 - 5.6	4 IP, 1 IOU w/14 ch
1988	UNISYS 2200/600			36 bits x 16 MW	266	17 - 52	4 IP, 4 IOU w/96 ch
1989	UNISYS 2200/400			36 bits x 16 MW	276	4 - 25	6 IP, 10 IOU w/90 ch
1990	UNISYS 2200/100			36 bits x 12 MW	281	1.6 - 2.9	2 IP, 1 IOU w/9 ch
1992	UNISYS 2200/900			36 bits x 512 MW	281	34 - 286	8 IP, 96 IOU w/384 ch
1994	UNISYS 2200/500			36 bits x 512 MW	281	8.5 - 71	8 IP, 32 IOP w/128 ch
1995	UNISYS 2200/700			36 bits x 512 MW	281	10.2 - 71	8 IP, 32 IOP w/128 ch
1995	UNISYS 2200/300			36 bits x 128 MW	281	2.2	1 IP, 8 IOP w/32 ch
1996	UNISYS 2200/500	Intel IPs		512 MW	281	10.2 - 71	8 IP, 32 IOP w/128 ch
1996	UNISYS 2200/3800	Intel IPs		1024 MW	285	27 - 190	8 IP, 12 IOP w/192 ch
1996	ClearPath 4800	Intel IPs		1 GW	285	8.5 - 238	8 IP, 12 IOP w/192 ch
1998	ClearPath 5800	Intel IPs		1 GW	285	39 - 286	8 IP, 12 IOP w/192 ch
1998	ClearPath 5600	Intel IPs		768 MW	285	8.5 - 132	4 IP, 6 IOP w/96 ch
1999	ClearPath 6600	Intel IPs		1 GW	287	50 - 410	8 IP, 12 IOP w/192 ch
2001	ClearPath (aka Dorado)	Intel IPs		8 GW	289	25 - 820	32 IP, 8 I/O w/678 ch
2004	Dorado 280	Intel IPs		8 GW	294	100 - 1230	32 IP, 8 I/O w/678 ch
2005	Dorado 380	Intel IPs		64 GW	294	50 - 1640	32 IP, 16 I/O w/421 ch
Note 1	The baseline for the Peformance column is the 1965 1107 Computer.						
Note 2	Thanks to Ron Q. Smith for the tabular data.						