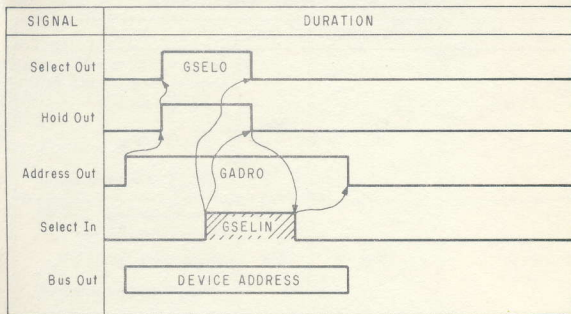
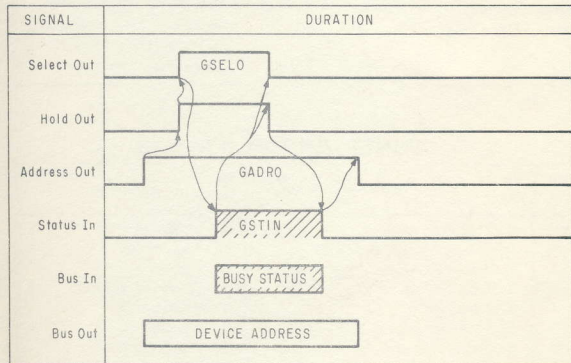


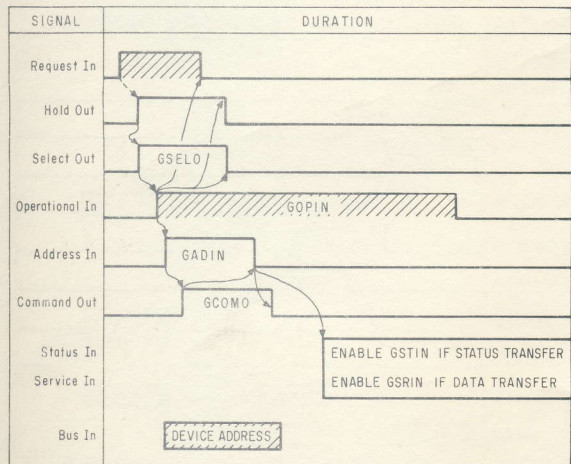
INITIAL SELECTION SEQUENCE (NO RESPONSE)



INITIAL SELECTION SEQUENCE (BUSY)



CONTROL UNIT INITIATED SEQUENCE



= Control Unit Response

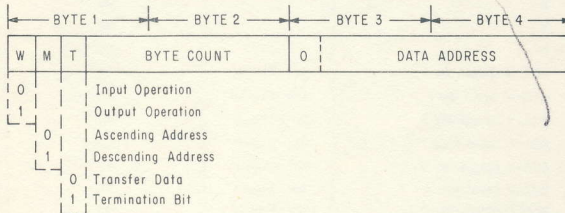
UNIVAC® MH2266 7/68

9200/9300 SYSTEMS

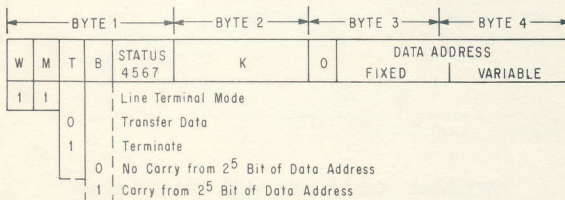
MULTIPLEXER CHANNEL

MAINTENANCE CARD

BUFFER CONTROL WORD



LINE TERMINAL BUFFER CONTROL WORD



I/O BUFFER CONTROL WORDS

54	58	5C	60	64	68	6C	70	74	78	7C
RESERVED FOR MULTIPLEXER CHANNEL BUFFER CONTROL WORDS										
5	6	7	8	9	A	B	C	D	E	F

200	204	208	20C	210	214	218	21C	220	224	228
MULTIPLEXER CHANNEL BUFFER										
40	41	42	43	44	45	46	47	48	49	4A

22C	230	234	238	23C
CONTROL WORDS				
4B	4C	4D	4E	4F

STATUS BYTE

Bit	0	1	2	3	4	5	6	7
Detail	Att.	Stat. Mod.	Cont. Unit End	Busy	Chan. End	Dev. End	Unit Chk.	Unit Except

CHANNEL DEVICE ADDRESS ASSIGNMENTS

DEVICE	FORMAT	HEXADECIMAL SUB-CHANNEL NUMBER
8410 Disc	Non-Shared	05
Row Punch	Non-Shared	06
1001 Card Controller	Shared	07
VI-C Tape NO. 1	Shared	08
VI-C Tape NO. 2	Shared	09
ICCU 418/1108-9300	Non-Shared	0A
0768 Printer	Non-Shared	0B
0920 Paper Tape Subsystem	Non-Shared	0C
*		0D
*		0E
*		0F
DCS-4 Output No. 1	Non-Shared	40
DCS-4 Input No. 1	Non-Shared	41
DCS-4 Output No. 2	Non-Shared	42
DCS-4 Input No. 2	Non-Shared	43
DCS-4 Output No. 3	Non-Shared	44
DCS-4 Input No. 3	Non-Shared	45
DCS-4 Output No. 4	Non-Shared	46
DCS-4 Input No. 4	Non-Shared	47
DCS-1 Output No. 1	Non-Shared	48
DCS-1 Input No. 1	Non-Shared	49
DCS-1 Output No. 2	Non-Shared	4A
DCS-1 Input No. 2	Non-Shared	4B
*		4C
*		4D
*		4E
*		4F

*Reserved for Future Control Units

CHANNEL TEST ROUTINE

INSTRUCTION	DEVICE ADDRESS	XF CODE	RESULT
XIOF or TIO	81 ₁₆	N/A	Interface Time Check Error and Selective Reset. Channel Status: [D ₁₆] = 98 ₁₆ [E ₁₆] = 1B ₁₆ [F ₁₆] = Not Device Address
XIOF or TIO	98 ₁₆	N/A	Device Parity Error, Input Bus Error and Selective Reset. [D ₁₆] = 6E ₁₆ [E ₁₆] = 1B ₁₆ [F ₁₆] = 98 ₁₆ (Device Address)
XIOF or Initial Load from Device Address 80 ₁₆	80 ₁₆	40	Initial Selection, BCW in 40 ₁₆ -43 ₁₆ . 23 ₁₆ is written into memory location specified by BCW. Normal interrupt upon completion. Initial load loads 23 ₁₆ in memory locations 00-4 ₁₆ . Ending BCW = 23BF0041.

CHANNEL ERROR STATUS

Mem. Loc.	Bit Pos.	Signal Function	Mem. Loc.	Bit Pos.	Signal Function
001D	0	Interface Error	001E	0	Status In
	1	Device Address Parity Error		1	Service Out
	2	Bus in Parity Error		2	Service In
	3	Address Out		3	Time Out Request
	4	Select Out		4	Suppress Out
	5	Operational In		5	Select In
	6	Address In		6	Terminate/KØ FF
	7	Command Out		7	Input Direction/K1 FF
			001F	0-7	Device Address Register

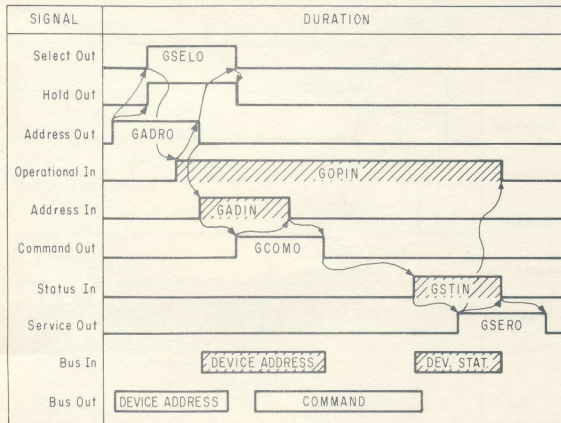
SPECIAL CHANNEL INSTRUCTIONS

DEVICE ADDRESS	XIO FUNTION CODE	FUNCTION
80 ₁₆	13	Inhibit Operator Request
80 ₁₆	23	Permit Operator Request
80 ₁₆	00 (TIO)	Test and Reset Operator Request
88 ₁₆	13	Inhibit LT Summary Interrupts
88 ₁₆	23	Permit LT Summary Interrupts
88 ₁₆	00 (TIO)	Test and Reset LT Summary Interrupts
90 ₁₆	23	Set One Second Delay
90 ₁₆	00 (TIO)	Test and Reset One Second Interrupt

FIRST SENSE BYTE

BIT	INDICATION
P	Parity (Odd)
0	Command Reject
1	Intervention Required
2	Bus Out Parity
3	Equipment Check
4	Data Check
5	Data Late
6	Undefined
7	Undefined

INITIAL SELECTION SEQUENCE (NORMAL)



▨ = Control Unit Response