

UNIVAC 1219 COMPUTER

REPERTOIRE OF INSTRUCTIONS

CODE	SYMBOL	DESCRIPTION	TIME μS	CODE	SYMBOL	DESCRIPTION	TIME μS
02	CMAL	Compare Y	4	66	JPAUNG	Jump AU Negative, Y	2
03	CMALB	Compare Y+B	4	67	JPALNG	Jump AL Negative, Y	2
04	SLSU	Selective Substitute	4	70	ENTALK	Enter AL, Y	2
05	SLSUB	Selective Substitute Y+B	4	71	ADDALK	Add U, 12 bits	2
06	CMSK	Masked Compare Y	4	72	STRICR	Store ICR, Y	4
07	CMSKB	Masked Compare Y+B	4	73	BJP	Decrement B, Jump, Y	2
10	ENTAU	Enter AU, Y	4	74	STRADR	Store Address, Y	4
11	ENTAUB	Enter AU, Y+B	4	75	STRSR	Store SR, Deactivate SR, Y	4
12	ENTAL	Enter AL, Y	4	76	RJP	Return Jump, Y	4
13	ENTALB	Enter AL, Y+B	4	5001	SIN	Set Input Active	2
14	ADDAL	Add Y, 18 bit	4	5002	SOUT	Set Output Active	2
15	ADDALB	Add Y+B, 18 bit	4	5003	SEXF	Set External Function Active	2
16	SUBAL	Subtract Y, 18 bit	4	5011	IN	Initiate Input Buff, k	6
17	SUBALB	Subtract Y+B, 18 bit	4	5012	OUT	Initiate Output Buff, k	6
20	ADDA	Add Y, 36 bit	6	5013	EXF	External Function	6
21	ADDAB	Add Y+B, 36 bit	6	5014	RTC	Enable Real-Time Clock	2
22	SUBA	Subtract Y, 36 bit	6	5015	INSTP	Terminate Input, k	2
23	SUBAB	Subtract Y+B, 36 bit	6	5016	OUTSTP	Terminate Output, k	2
24	MULAL	Multiply Y	14	5017	EXFSTP	Terminate External Function, k	2
25	MULALB	Multiply Y+B	14	5020	SRSM	Set Resume ff (Intercomp)	2
26	DIVA	Divide, Y	14	5021	SKPIIN	Skip Input Inact, k	2
27	DIVAB	Divide, Y+B	14	5022	SKPOIN	Skip Output Inact, k	2
30	IRJP	Indirect RJP, Y	6	5023	SKPFIN	Skip on Ext. Fct. Inact.	2
31	IRJPB	Indirect RJP, Y+B	6	5024	WTFI	Wait for Interrupt	2
32	ENTB	Enter B, Y	4	5026	OUTOV	Force Output One Word, k	2
33	ENTBB	Enter B, Y+B	4	5027	EXFOV	Force Ext Function One Word, k	2
34	JP	Jump, Y	2	5030	RIL	Remove Interrupt Lockout	2
35	JPB	Jump, Y+B	2	5032	EXL	Remove Ext Interrupt Lockout	2
36	ENTBK	Enter, B, U	2	5034	SIL	Set Interrupt Lockout	2
37	ENTBKB	Modify B, U	2	5036	SXL	Set Ext Interrupt Lockout	2
40	CL	Store Zero, Y	4	5041	RSHAU	Right Shift AU, k	4-10
41	CLB	Store Zero, Y+B	4	5042	RSHAL	Right Shift AL, k	4-10
42	STRB	Store, B, Y	4	5043	RSHA	Right Shift A, k	4-20
43	STRBB	Store B, Y+B	4	5044	SF	Scale A Left, k, SF	4-20
44	STRAL	Store AL, Y	4	5045	LSHAU	Left Shift AU, k	4-10
45	STRALB	Store AL, Y+B	4	5046	LSHAL	Left Shift AL, k	4-10
46	STRAU	Store AU, Y	4	5047	LSHA	Left Shift A, k	4-20
47	STRAUB	Store AU, Y+B	4	5050	SKP	Skip Console Key, k	2
51	SLSET	Selective Set (IOR), Y	4	5051	SKPNBO	Skip No Borrow	2
52	SLCL	Selective Clear (AND), Y	4	5052	SKPOV	Skip Overflow	2
53	SLCP	Selective Complement (XOR), Y	4	5053	SKPNOV	Skip No Overflow	2
54	IJPEI	Indirect Jump (RIL), Y	4	5054	SKPODD	Skip L(AU, AL) Odd Parity	2
55	IJP	Indirect Jump, Y	4	5055	SKPEVN	Skip L(AU, AL) Even Parity	2
56	BSK	Increment B, Skip, Y	4	5056	STOP	Stop Console Key, k	2
57	ISK	Decrement Index, Skip, Y	6	5057	SKPNR	Skip No Resume ff (Intercomp)	2
60	JPAUZ	Jump AU Zero, Y	2	5060	RND	Round AU	2
61	JPALZ	Jump AL Zero, Y	2	5061	CPAL	Complement AL	2
62	JPAUNZ	Jump AU Not Zero, Y	2	5062	CPAU	Complement AU	2
63	JPALNZ	Jump AL Not Zero, Y	2	5063	CPA	Complement A	2
64	JPAUP	Jump AU Positive, Y	2	5072	ENTICR	Enter ICR, k	2
65	JPALP	Jump AL Positive, Y	2	5073	ENTSR	Enter, SR, k	2

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ASSIGNED MEMORY ADDRESSES

CONTROL MEMORY (Standard)

Address	Assignment
00000	Fault Interrupt Entrance Register
00001-00010	8 Index Registers
00011	Inter-Computer Time-Out Interrupt Register
00012	Real-Time Clock Interrupt Register
00013	Clock Overflow Interrupt Register
00014	Real-Time Clock Monitor Word Register
00015	Real-Time Clock Incrementing Register
00016	Synchronizing Interrupt Register
00017	Scale Factor Shift Count
00020-00037	Continuous Data Mode (Channels 0-7)
00040-00057	Output Buffer Control Registers (Channels 0-7)
00060-00077	Input Buffer Control Registers (Channels 0-7)
Optional with UNIVAC 1219 Computers equipped with 16 I/O Channels:	
00200-00217	
00220-00237	Continuous Data Mode (Channels 8-15)
00240-00257	Output Buffer Control Registers (Channels 8-15)
00260-00277	Input Buffer Control Registers (Channels 8-15)

MAIN MEMORY

00100-00117	External Interrupt Registers (Channels 0-7)
00120-00137	UNASSIGNED
00140-00157	Output Monitor Registers (Channels 0-7)
00160-00177	Input Monitor Registers (Channels 9-7)
00300-00317	External Interrupt Registers (Channels 8-15)
00320-00337	UNASSIGNED
00340-00357	Output Monitor Registers (Channels 8-15)
00360-00377	Input Monitor Registers (Channels 8-15)
00400-00477*	UNASSIGNED
00600-00677*	UNASSIGNED

NONDESTRUCTIVE READOUT MEMORY

00500-00537	Bootstrap Program (initial input routine)
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*These addresses are in control memory when 256 words of control memory are used.

UNIVAC

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